WHAT IS CLAIMED IS:

1. An electronic circuit unit comprising thin film circuit elements including capacitors, resistors, and inductance elements formed on an alumina substrate, and a semiconductor bare chip having a first transistor wire bonded to the alumina substrate, wherein only an emitter resistor, out of a base bias voltage dividing resistor and the emitter resistor of the first transistor, is trimmed to set a current value of the first transistor.

2. The electronic circuit unit according to claim 1, wherein the semiconductor bare chip has a second transistor connected to the first transistor in series, and only the emitter resistor of the first transistor, out of base bias voltage dividing resistors and emitter resistors of the first and second transistors, is trimmed to set the current value of the first and second transistors.

3. An electronic circuit unit comprising circuit elements including thin film capacitors, resistors, and inductance elements formed on an alumina substrate, and a semiconductor bare chip having a first transistor wire bonded to the alumina substrate, wherein thin film base bias voltage dividing resistors to apply a voltage to a base of the first transistor are formed proximate to each other on the alumina substrate.

4. The electronic circuit unit according to claim 3, wherein the semiconductor bare chip has a second transistor connected to the first transistor in series, and thin film base bias voltage dividing resistors of the first and second transistors are formed proximate to each other on the alumina substrate.

5. The electronic circuit unit according to claim 3, wherein at least a part of the base bias voltage dividing resistors are located on a plurality of lines in parallel.

6. The electronic circuit unit according to claim 4, wherein at least a part of the base bias voltage dividing resistors are located on a plurality of lines in parallel.